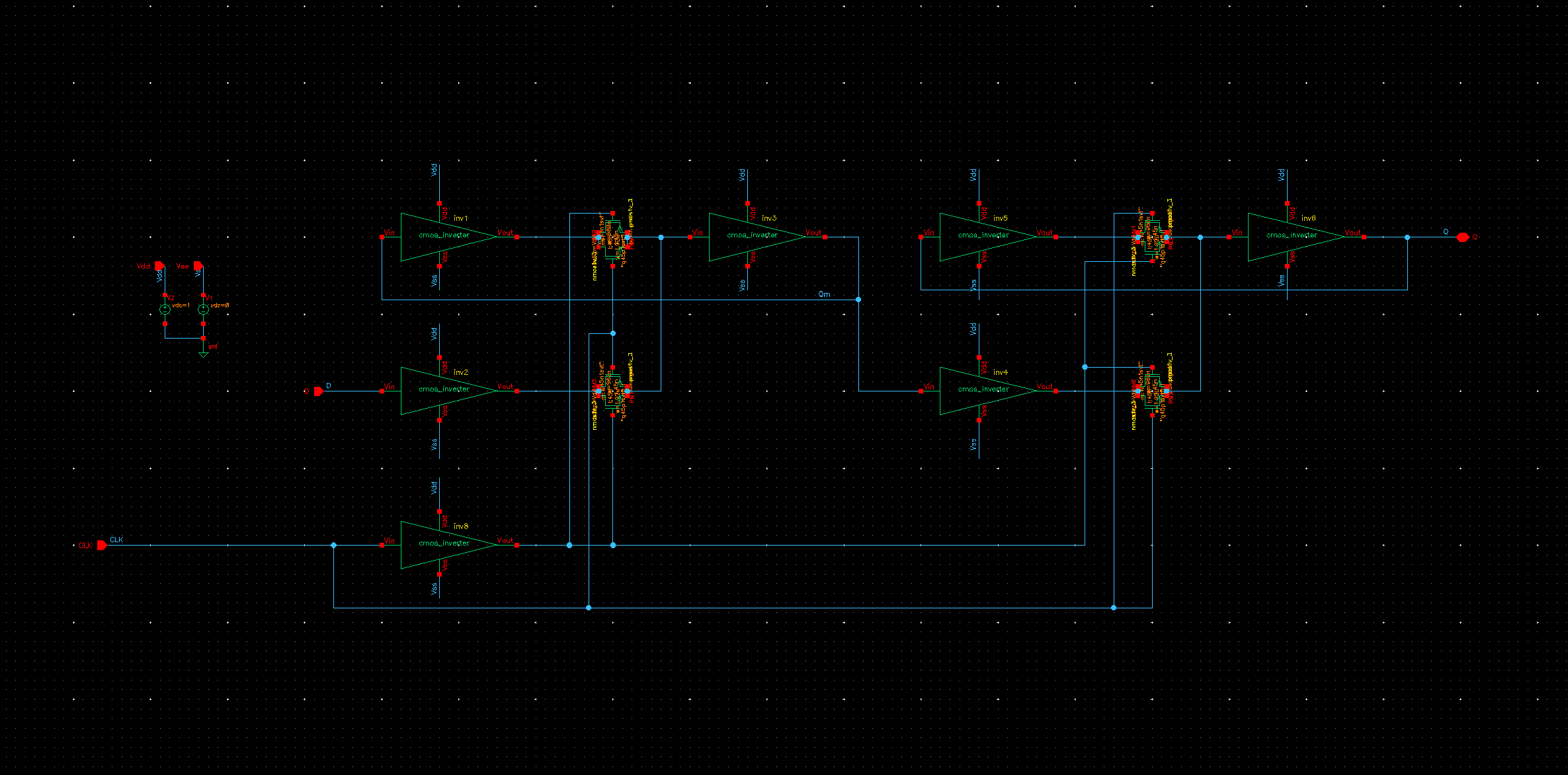
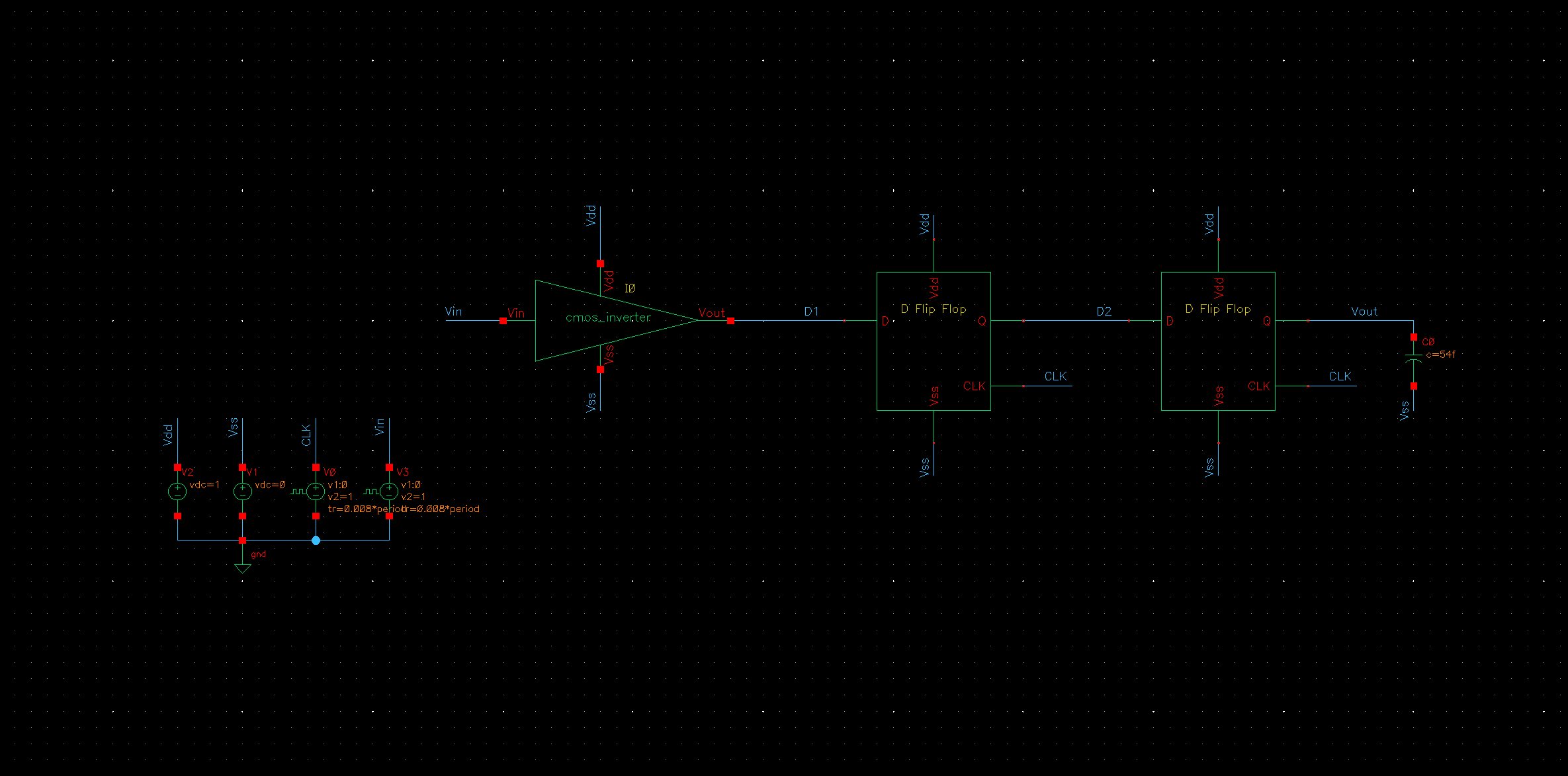
# Homework 6

## Flip Flop

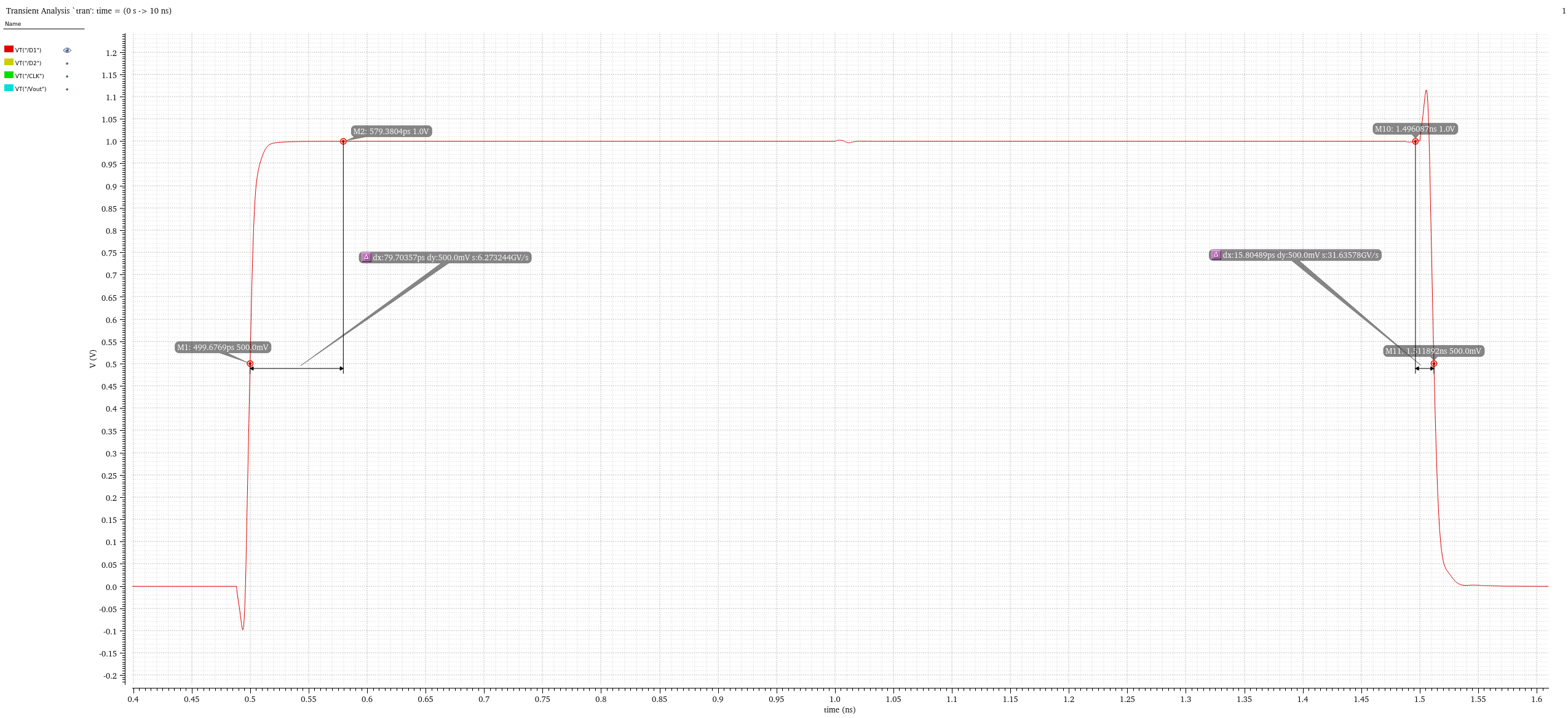


1.92um PMOS, 0.96um NMOS

## Flip Flop Test Bench



54fF Capacitor on Vout

Setup Time & Hold Time

## CLK to Q time (Falling and Rising Q)

